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[PDF] ► **Processor allocation and checkpoint interval selection in cluster computing systems**

JS Plank, MG Thomason - Journal of Parallel and distributed Computing, 2001 - citeseerx.ist.psu.edu

... replacement processor is available, then the application is halted until one becomes ...
 user must execute a long-running application on a cluster computing system ...

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Implementing global memory management in a workstation cluster- ► [psu.edu](#) [PDF]

MJ Feeley, WE Morgan, EP Pighin, AR Karlin, HM ... - Proceedings of the fifteenth ACM symposium on Operating ..., 1995 - portal.acm.org

... We believe that as processor performance increases and communication ... the cluster contains workstations, disk servers. ... when a node is about to replace a page ...

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System upgrade and processor service

R Bealkowski, S Dhawan, KC Hinz, PM Thomsen - US Patent 6,378,027, 2002 - Google Patents

... L/56 SERVICE PROCESSOR(S) J 58 I POWER UP PROCESSOR CLUSTER 1/60 ADD CLUSTER TO PROCESSOR POOL (Fig. 3 Page 4. US 6,378,027 BI SYSTEM UPGRADE AND PROCESSOR ...

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Web search for a planet: The Google cluster architecture- ► [fau.edu](#) [PDF]

LA Barroso, J Dean, U Holzle - IEEE micro, 2003 - ieeeexplore.ieee.org

... processor servers can be quite substantial, at least for a ... Assuming tools to install and upgrade software on groups ... the cost of monitoring a cluster using a ...

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TNet: A reliable system area network- ► [4x.com](#) [PDF]

RW Horst, TC Inc, CA Cupertino - IEEE Micro, 1995 - ieeeexplore.ieee.org

... TNet is not a direct replacement for any exist- ing type of local area network or I/O bus. ... Figure 1. CPU cluster connected with a typical high-speed LAN. ...

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Online track processor for the CDF upgrade

EJ Thomson, C Ciobanu, JY Chung, J Gerstenlager, ... - IEEE Transactions on Nuclear Science, 2002 - ieeeexplore.ieee.org

... Page 7. THOMSON et al.: ONLINE TRACK PROCESSOR FOR THE CDF UPGRADE 1069 Fig. ... The logic is designed to report the median bin of this cluster of bins. ...

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COSYN: hardware-software co-synthesis of embedded systems- ► [york.ac.uk](#) [PDF]

BP Dave, G Lakshminarayana, NK Jha - Proceedings of the 34th annual conference on Design ..., 1997 - portal.acm.org

... Similarly, while allocating a cluster to a general-purpose processor, it is made sure that the memory capacity of the PE is not exceeded. ...

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5 Formal Verification of the Gigamax Cache Consistency Protocol

K McMillan, J Schwalbe - Shared Memory Multiprocessing, 1992 - books.google.com

... 5. The **processor** in **cluster 2** invalidates its copy by cache **replacement**, then encounters a read miss and issues a read public command, which is Page 134. ...

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... density map improvement and structure validation on a Linux multi-CPU web **cluster**: The TB Structural ... - [fullerton.edu PDF](#)

V Reddy, S Swanson, B Segelke, KA Kantardjieff, JC ... - 2003 - [dx.doi.org](#)

... programs, a user-friendly web service for automated molecular **replacement**, map improvement ... cycles are executed on a low-cost Linux multi-CPU **cluster** using the ...

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Fault-tolerant boot strap mechanism for a multiprocessor system

M Karnik, J Batz, K Tiruvallur, A Glew, F Binns, S ... - US Patent 5,724,527, 1998 - Google Patents

... The invention also supports upgrade strategies for which ... test and initialization routines, each **processor** vidual devices ... as part of a single **cluster** of begins ...

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